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L10	l9 and electronic near (money or funds or currency or cash or purse)	13	L10
L9	l5 and (ic or "integrated circuit")	54	L9
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L7	L5 and line	96	L7
L6	L5 and public near line	0	L6
L5	L4 and light near signals	100	L5
L4	L3 and external near device	3164	L4
L3	enter$ near data	50700	L3
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L1	process$ near data near (ic or "integrated circuit")	561	L1

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L2: Entry 12 of 14   File: USPT   Jun 8, 1999

US-PAT-NO: 5910652
DOCUMENT-IDENTIFIER: US 5910652 A

TITLE: Portable terminal apparatus for an IC card for supplying a power source voltage in a predetermined timing

DATE-ISSUED: June 8, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kuriyama; Ryouichi	Zushi			JP

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Kabushiki Kaisha Toshiba	Kawasaki			JP	03

APPL-NO: 08/ 791783   [PALM]
DATE FILED: January 29, 1997

FOREIGN-APPL-PRIORITY-DATA:

COUNTRY	APPL-NO	APPL-DATE
JP	8-016034	January 31, 1996

INT-CL: [06] G06 K 19/06

US-CL-ISSUED: 235/492; 235/380, 902/26
US-CL-CURRENT: 235/492; 235/380, 902/26

FIELD-OF-SEARCH: 235/382, 235/436, 235/441, 235/444, 235/482, 235/485, 235/486, 235/492

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

　Search Selected　　 　Search ALL　　 　Clear　

	PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
☐	4755660	July 1988	Nakano	235/380
☐	5015830	May 1991	Masuzawa et al.	235/441
☐	5146068	September 1992	Ugawa et al.	235/441
☐	5247164	September 1993	Takahashi /	235/492

☐ <u>5450365</u>　　September 1995　　Adachi　　　　　365/226

☐ <u>5541985</u>　　July 1996　　　Ishii et al.　　379/111

FOREIGN PATENT DOCUMENTS

FOREIGN-PAT-NO	PUBN-DATE	COUNTRY	US-CL
0 237 883	September 1987	EP	235/492
WO 95/04328	February 1995	WO	

ART-UNIT: 286

PRIMARY-EXAMINER: Hajec; Donald

ASSISTANT-EXAMINER: Fureman; Jared J.

ATTY-AGENT-FIRM: Pillsbury Madison & Sutro LLP

ABSTRACT:

An apparatus for performing processing on an integrated circuit medium includes a mechanism for performing transfer to and from the integrated circuit medium held therein, and a controller adapted to supply a power supply voltage to the integrated circuit medium after a predetermined condition has been satisfied with the integrated circuit medium set in the apparatus and to stop the supply of the power supply voltage when predetermined processing is completed on the integrated circuit medium.

5 Claims, 6 Drawing figures

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L2: Entry 13 of 14   File: USPT   Sep 29, 1998

US-PAT-NO: 5815658
DOCUMENT-IDENTIFIER: US 5815658 A

TITLE: Portable terminal apparatus for IC card compatible with a plurality of applications

DATE-ISSUED: September 29, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kuriyama; Ryouichi	Zushi			JP

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Kabushiki Kaisha Toshiba	Kawasaki			JP	03

APPL-NO: 08/ 791782   [PALM]
DATE FILED: January 29, 1997

FOREIGN-APPL-PRIORITY-DATA:

COUNTRY	APPL-NO	APPL-DATE
JP	8-067962	March 25, 1996

INT-CL: [06] G06 F 11/34

US-CL-ISSUED: 395/186; 235/379, 235/380, 235/492
US-CL-CURRENT: 713/200; 235/379, 235/380, 235/492

FIELD-OF-SEARCH: 395/186, 235/379, 235/380, 235/492

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

| Search Selected | | Search ALL | | Clear |

	PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
☐	5592819	January 1997	Shona	395/186

ART-UNIT: 286

PRIMARY-EXAMINER: Pitts; Harold

ATTY-AGENT-FIRM: Cushman Darby & Cushman IP Group of Pillsbury Madison & Sutro LLP

ABSTRACT:

A data processing apparatus for an IC card includes an interface section for receiving and accessing an integrated circuit medium, a first application function for performing data processing for the integrated circuit medium on the basis of the first application, a second application function for performing data processing on the basis of the second application, and a control unit for selecting one of the first and second application functions on the basis of an instruction from a keyboard and controlling it so as to perform data processing.

11 Claims, 6 Drawing figures

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L2: Entry 13 of 14   File: USPT   Sep 29, 1998

DOCUMENT-IDENTIFIER: US 5815658 A
TITLE: Portable terminal apparatus for IC card compatible with a plurality of
applications

Abstract Text (1):
A data processing apparatus for an IC card includes an interface section for
receiving and accessing an integrated circuit medium, a first application function
for performing data processing for the integrated circuit medium on the basis of
the first application, a second application function for performing data processing
on the basis of the second application, and a control unit for selecting one of the
first and second application functions on the basis of an instruction from a
keyboard and controlling it so as to perform data processing.

Brief Summary Text (2):
The present invention relates to a portable terminal apparatus for an IC card which
selectively reads and displays various transaction data, amount data, and the like
from an IC card used as, e.g., a credit card and electronic money and supporting a
plurality of applications.

Brief Summary Text (4):
When an IC card of this type is used as, e.g., a credit card and electronic money,
it is very convenient to selectively read and display various transaction data,
amount data, and the like stored in the IC card, as needed.

Brief Summary Text (11):
According to the present invention, there is provided a data processing apparatus
for an integrated circuit medium, comprising: means for receiving and accessing the
integrated circuit medium storing data; first processing means for performing data
processing for the integrated circuit medium on the basis of a first application;
second processing means for performing data processing for the integrated circuit
medium on the basis of a second application different from the first application;
means for inputting an instruction concerning data processing of the integrated
circuit medium; means for selecting one of the first processing means and the
second processing means in accordance with the instruction input from the input
means; and means for controlling one of the first processing means and the second
processing means which is selected by the selecting means so as to perform data
processing.

CLAIMS:

1. A data processing apparatus for an integrated circuit medium, comprising:

means for receiving and accessing the integrated circuit medium storing data;

first processing means for performing data processing for the integrated circuit
medium on the basis of a first application;

second processing means for performing data processing for the integrated circuit
medium on the basis of a second application different from the first application;

means for inputting an instruction concerning <u>data processing of the integrated</u> <u>circuit</u> medium;

means for selecting one of the first processing means and the second processing means in accordance with the instruction input from the inputting means; and

means for controlling one of the first processing means and the second processing means which is selected by the selecting means so as to perform data processing.

6. An apparatus according to claim 1, further comprising:

third processing means for performing <u>data processing for the integrated circuit</u> medium on the basis of at least one third application different from the first and second applications;

means for selecting one of the first, second, and third processing means in accordance with the instruction input from the inputting means; and

means for controlling one of the first, second, and third processing means which is selected by the selecting means so as to perform data processing.

7. An apparatus according to claim 6, wherein the first processing means includes:

means for performing <u>data processing for the integrated circuit</u> medium on the basis of an application concerning a credit card,

the second processing means includes:

means for performing <u>data processing for the integrated circuit</u> medium on the basis of an application concerning point accumulation, and

the third processing means includes:

means for performing <u>data processing for the integrated circuit</u> medium on the basis of an application concerning a prepaid card.

9. A data processing apparatus for an integrated circuit medium, comprising:

means for receiving and accessing the integrated circuit medium storing data;

first processing means for performing <u>data processing for the integrated circuit</u> medium on the basis of a first application;

second processing means for performing <u>data processing for the integrated circuit</u> medium on the basis of a second application different from the first application;

third processing means for performing <u>data processing for the integrated circuit</u> medium on the basis of at least one third application different from the first and second applications;

means for inputting an instruction concerning <u>data processing of the integrated</u> <u>circuit</u> medium;

means for selecting one of the first, second, and third processing means in accordance with the instruction input from the inputting means;

means for controlling one of the first, second, and third processing means which is selected by the selecting means so as to perform data processing;

means for storing necessity/nonnecessity data concerning necessity/nonnecessity of

key collation in correspondence with one of the first, second, and third applications;

means for determining whether the key collation is necessary by reading the necessity/nonnecessity data corresponding to one of the first, second, and third applications which is selected by the selecting means;

means for displaying a message which causes an operator to input data for the key collation when the determining means determines that the key collation is necessary; and

means for reading and displaying data of the integrated circuit medium on the basis of the selected one of the first, second, and third applications when the determining means determines that the key collation is unnecessary.